

CLAIMS:

1. A receiver signal strength indication circuit comprising:
means (A1-A4) for discretely controlled amplifying an input signal;
means (NF, log, ADC) coupled to an output of the amplifying means (A1-A4)
for furnishing a receiver signal strength indication.
- 5 2. A receiver signal strength indication circuit as claimed in claim 1, wherein the
receiver signal strength indication furnishing means (NF, log, ADC) include a narrow filter
(NF) for measuring a power of the input signal with reduced influence from neighboring
channels.
- 10 3. An integrated tuner comprising a receiver signal strength indication circuit as
claimed in claim 1, wherein the amplifying means (A1-A4, SF1, SF2, M) include selectivity
filtering means (SF1, SF2).
- 15 4. An integrated tuner comprising a receiver signal strength indication circuit as
claimed in claim 1, wherein the amplifying means (A1-A4, SF1, SF2, M) include a mixer
(M).